

# NANYA (e)MCP Part Numbering Guide



**NM** = MCP  
**NE** = eMCP

## Product Family

### [MCP]

- 1** = SLC NAND + LPDDR2
- 4** = SLC NAND + LPDDR4X

### [eMCP]

- 1** = MLC NAND + LPDDR3
- 4** = SMLC NAND + LPDDR4X
- 9** = TLC NAND + LPDDR3
- 6/A** = TLC NAND + LPDDR4X

## NAND Organization

### [MCP]

Density	Config
<b>1</b> = 1Gb	<b>8</b> = x8
<b>2</b> = 2Gb	<b>F</b> = x16
<b>4</b> = 4Gb	
<b>8</b> = 8Gb	

### [eMCP]

Density	Config
<b>4</b> = 4GB	<b>8</b> = x8
<b>8</b> = 8GB	<b>F</b> = x16
<b>F</b> = 16GB	
<b>A</b> = 32GB	
<b>B</b> = 64GB	

## DRAM Organization

Density	Config
<b>1</b> = 1Gb	<b>N</b> = x16
<b>2</b> = 2Gb	<b>K</b> = x32
<b>4</b> = 4Gb	
<b>8</b> = 8Gb	
<b>F</b> = 16Gb	
<b>A</b> = 32Gb	

## NAND Supplier

### [MCP]

- S** = KioXia
- M** = MXIC

### [eMCP]

- A** = KioXia
- W** = WD

## Grade

- N/A** = Commercial Grade
- E** = Wide Temp. Grade
- I** = Industrial Grade

## DRAM Speed

- B** = 1066Mbps @RL=8
- C/D** = 1866Mbps @RL=14
- G** = 2133Mbps @RL=16
- E** = 3733Mbps @RL=32
- F** = 4267Mbps @RL=36

## NAND Speed

- 3** = SLC 40MHz
- M** = Bics3 MLC 100MHz
- K** = Bics3 TLC 200MHz
- A** = Bics5 TLC 266MHz

## Package Code

- (RoHS+Halogen-Free)
- E** = 149-Ball BGA
- J/L** = 162-Ball BGA
- 7** = 221-Ball BGA
- I** = 254-Ball BGA

## Device Version

- A** = 1<sup>st</sup> version
- B** = 2<sup>nd</sup> version
- C** = 3<sup>rd</sup> version
- ⋮
- ⋮

## JEDEC Version

- AX** = eMMC5.1

## DRAM Type

- L** = LPDDR2/LPDDR3
- P** = LPDDR4X