NANYA Low Power DRAM Part Numbering Guide

NT | 6C | L | 128M32 | B | M – H1 | I

NT = Component

Product Family

6T = LPDDR2
6C = LPDDR3
6A = LPDDR4/4X
6B = LPDDR5/5X

Interface & Power

(V_{DD1} , V_{DD2} , V_{DDQ} , V_{DDCA})

L = HSUL_12 (1.8V, 1.2V, 1.2V, 1.2V)
N = LVSTL (1.8V, 1.1V, 1.1V, N/A)
P = LVSTL (1.8V, 1.1V, 0.6V, N/A)
T = LVSTL (1.8V, 1.1V, 1.1V/0.6V, N/A)
R = LVSTL (1.8V, 1.05V, 0.9V, 0.5V/0.3V)

Organization (Depth, Width):

M=Mono ; T=DDP ; F=QDP
1Gb = 64M16 = 32M32
2Gb = 128M16 = 64M32 = 64T32
4Gb = 256M16 = 128M32 = 128T32 = 64T64
8Gb = 512M16 = 256M32 =256T32= 128T64
16Gb = 1024M16 = 512T32 = 256T64 = 256F64
32Gb = 1024T32 = 1024F32 = 512F64
64Gb = 1024F64 = 2048F32

Device Version

A = 1st version
B = 2nd version
C = 3rd version
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Package Code

(RoHS+Halogen-Free)

[LPDDR2; LPDDR3]

A = 134-Ball BGA
M/P = 178-Ball BGA
7 = 253-Ball BGA
9/Q = 168-Ball PoP
R/H = 216-Ball PoP
4/J = 256-Ball PoP

[LPDDR4/4X; LPDDR5/5X]

C/Y = 200-Ball BGA (0.41mm solder ball)
L/V = 200-Ball BGA (0.35mm solder ball)
3 = 315-Ball BGA
N = 376-Ball PoP

Grade

[Commercial]

N/A = Commercial Grade
T = Quasi Industrial Grade

[Industrial]

I = Industrial Grade

[Automotive]

J = Grade 3
H = Grade 2
K = Grade 1

Speed

G0 = 1066Mbps
H1 = 1866Mbps
H0 = 2133Mbps
J2 = 3733Mbps
J1 = 4267Mbps
K2 = 7500Mbps
K1 = 8533Mbps