# NANYA Low Power DRAM Part Numbering Guide

NT | 6C | L | 128M32 | B | M – H1 | I

NT = Component

## Product Family

6T = LPDDR2
6C = LPDDR3
6A = LPDDR4/4X
6B = LPDDR5/5X

## Interface & Power

($V_{DD1}$ , $V_{DD2}$ , $V_{DDQ}$ , $V_{DDCA}$)

L = HSUL_12 (1.8V, 1.2V, 1.2V, 1.2V)
N = LVSTL (1.8V, 1.1V, 1.1V, N/A)
P = LVSTL (1.8V, 1.1V, 0.6V, N/A)
T = LVSTL (1.8V, 1.1V, 1.1V/0.6V, N/A)
R = LVSTL (1.8V, 1.05V, 0.9V, 0.5V/0.3V)

## Organization (Depth, Width):

M=Mono ; T=DDP ; F=QDP
512Mb = 32M16 = 16M32
1Gb = 64M16 = 32M32
2Gb = 128M16 = 64M32
4Gb = 256M16 = 128M32 = 128T32 = 64T64
8Gb = 512M16 = 256M32 =128T64 = 256T32
16Gb = 512T32 = 256T64 =256F64
32Gb = 1024F32 = 512F64
64Gb = 2048F32

## Device Version

A = 1st version
B = 2nd version
C = 3rd version
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## Package Code

(RoHS+Halogen-Free)

### LPDDR2

A = 134-Ball FBGA
9/Q = 168-Ball PoP
R = 216-Ball PoP
5 = 220-Ball PoP

### LPDDR3

M/P = 178-Ball FBGA
7 = 253-Ball FBGA
Q = 168-Ball PoP
R/H = 216-Ball PoP
4/J = 256-Ball PoP

### LPDDR4/4x

C/Y = 200-Ball FBGA (0.41mm solder ball)
L/V = 200-Ball FBGA (0.35mm solder ball)
N = 376-Ball PoP

## Grade

### Commercial Grade

N/A = Commercial Grade
T = Quasi Industrial Grade

### Industrial Grade

I = Industrial Grade

### Automotive Grade

J = Grade 3
H = Grade 2
K = Grade 1

## Speed

G0 = 1066Mbps @RL=8
H1 = 1866Mbps @RL=14
H0 = 2133Mbps @RL=16
J2 = 3733Mbps @RL=32
J1 = 4267Mbps @RL=36